

REMARKS

This Amendment is in response to the Office Action of April 23, 2007 in which claims 1-14 were rejected.

Drawings

Labelled drawings required by the Examiner have been provided in the Appendix hereof. Withdrawal of the objection to the drawings is requested.

Indefiniteness Rejection

Claims 1-14 are rejected as being indefinite for failing to particularly point out and distinctly claim the subject matter of the invention. There is no indication in the claims of what type of acceleration is realized or how to realize the acceleration.

In order to overcome this rejection, in claim 1 the first element has been amended from “an accelerator for accelerating data processing operations” to “an accelerator for accelerating *cryptographic* data processing operations” to further describe the accelerator more distinctly.

Support for the change: page 3, lines 32-37 and page 13, lines 31-33.

Novelty Rejection

The Examiner has rejected claims 1-2, 5 and 11-13 as being anticipated by *Grohoski et al.* The features of claims 2 and 3 have been added to claim 1 to overcome the novelty rejection. Amended claim 1 now includes the features of the accelerator of the electronic device comprising a configuration register, and the electronic device having a secure mode and a normal mode which modes are indicated by the configuration register. Considering *Grohoski et al* to be closest prior art, these features are not shown nor in any way indicated in *Grohoski et al.* Thus the present invention is novel with respect to the disclosure of *Grohoski et al.*

Obviousness rejection

Claims 3-4, 6-9, and 14 are rejected as being unpatentable over *Grohoski et al* in view of *Aaro et al*.

The invention

A difference in subject matter of new independent claim 1 as compared to the disclosure of *Grohoski et al* is that the electronic device of the present invention has a secure mode and a normal mode, which modes are indicated by a configuration register and which register is set by a processor in the device.

This has the technical effect that the operation of the device is secured by separating the processing of normal data and the processing of secure data. The technical problem to be solved in the light of the disclosure of *Grohoski et al* is thus how to make processing of data more secure.

The present invention solves this technical problem by separating the normal data processing and secure data processing by having a normal mode and a secure mode, which two modes are set in a register by a processor, whereby the present mode is indicated in the register so that when an application is to be performed, the processing mode may be checked in the register.

*Grohoski et al* disclose a system for processing a cryptographic packet, where the system comprises a host CPU and a cryptographic co-processor. The cryptographic co-processor is utilized to perform cryptographic processes parallel to other processes which are processed in the CPU. No specific modes of operation are disclosed. Again, *Grohoski et al* is regarded to constitute the closest prior art.

*Aaro et al* disclose a device for secure transactions, e.g. a mobile phone, having two modes of operation: a first mode when the phone functions as a conventional mobile phone and a secure mode for applications relating to secure transactions (column 3, lines 30-35). The mobile phone comprises a controller, which controls the device in a first mode of operation, and a secure part, which controls the device in a second secure mode of operation. The two modes may be controlled by separate processors that are included in the mobile phone part 1, Fig. 1 and the secure part 10, Fig. 1, Fig. 2, respectively. However, the two processors are not arranged to speed up the processing of cryptographic data, but are arranged to

separate mobile phone functionality and functions for secure transactions in the two respective modes. Thus, there is no arrangement for a cryptographic accelerator disclosed in *Aaro et al.* Furthermore, the device has a button 8 which the user utilizes to set the mode of the telephone. The mode is then indicated by a LED light 9 to the accelerator (column 3, lines 35-43, Fig. 1). The selection of the current mode is always done by the user. Thus, applicant respectfully disagrees with the Examiner and avers that *Aaro et al.* do not teach that a configuration register is arranged to indicate to *an accelerator* whether secure mode or normal mode *is set*, but rather that a user operates a manual button to set a switch and thereby selects the operation mode of the device. Consequently, there are features of new claim 1 that are neither disclosed by *Grohoski et al.* nor by *Aaro et al.* Furthermore, it will be evident to anyone that developing the device obtainable by means of combining *Grohoski et al.* and *Aaro et al.* to include those features would require inventive skill that is beyond the capability of the person of ordinary skill in the art.

Starting from *Grohoski et al.* as the closest prior, it is clear from the above reasoning that a skilled person would not find the solution to the technical objective problem formulated in the above by turning to *Aaro et al.* In the light of *Grohoski et al.*, there is no teaching in *Aaro et al.* which would lead the person of ordinary skill to the solution provided by the present invention. Consequently, the present invention is not rendered obvious to the person of ordinary skill.

The objections and rejections of the Office Action of April 23, 2007, having been obviated by amendment or shown to be inapplicable, withdrawal thereof is requested and passage of claims 1-14, as amended, to issue is earnestly solicited.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Francis J. Maguire". The signature is fluid and cursive, with the first name "Francis" being more prominent.

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IN THE DRAWINGS:

Please cancel the as-filed sheets 1-4 and substitute the REPLACEMENT SHEETS in the APPENDIX hereof as required by the Examiner. Also in the appendix are ANNOTATED SHEETS SHOWING CHANGES in which English legends have been added.